SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada) Seshadri Rao Knowledge Village, Gudlavalleru

Office of the Controller of Examinations

M.Tech I Semester (R20/R21) Regular Examinations, February 2024

TIME TABLE Timings: 01:30 p.m. to 04:30 p.m. Date: 29-01-2024 05.02.2024 07.02.2024 09.02.2024 12,02,2024 14.02.2024 15.02.2024 **Specialization** (Monday) (Wednesday) (Friday) (Monday) (Wednesday) (Thursday) Research Advanced Structural Advanced R.C. Advancements in Constitution of Structural Theory of Elasticity Methodology & IPR Analysis Design Concrete Technology India (BA3902) **Engineering** (CE3902) (BA3901) (CE3901) (CE3903) (CE3907) Testing and Research **VLSI Design &** Advanced Constitution of CMOS VLSI Design Testability of VLSI Parallel Processing Methodology & IPR **Embedded** Microcontrollers India (BA3902) (EC4901) Circuits (EC4908) (BA3901) **Systems** (EC4902) (EC4903) CSE Advanced Data Research (Artificial Constitution of Structures and Data Science Digital Image Internet of Things Methodology & IPR Intelligence & India (BA3902) Algorithm Analysis (CS4902) Processing (CS4903) (CS4906) (BA3901) Machine (CS4901) Learning)

Note: Seating arrangement will be notified on the day of each examination half an hour before the commencement of the examination.

Principal

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